

Net ID: \_\_\_\_\_ (no names, please)

You may use only the provided reference materials. All figures are on the last pages. You may use a calculator, either a four-function or a scientific calculator. You may not use a programmable calculator. The test is worth 100 pts, you are given 1 pt for free.

Part I: (75 pts)

- a. (5 pts) Write C code that configures PORTB for the IO shown in the figure for problem (a) on the Figure sheet. The internal weak pullup must be enabled. Do not assume any default bit values.

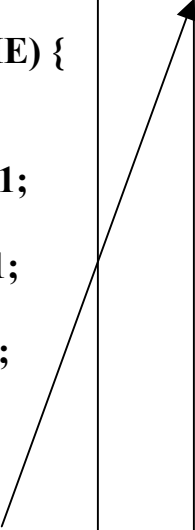
```
TRISB6 = 0; TRISB7 = 1; TRISB3 = 1 ;RBPU = 0;
```

- b. (15 pts) Assuming the IO configuration of the previous problem, write a *while(1){}* loop that implements the LED/Switch IO state machine shown for problem (b) in the figures. Either use a *switch()* statement approach or a *if-then-else* approach. Assume you have available the *DelayMs()* function for blinking the LED; you do NOT have to include debounce delays for the switch input.

```
while(1) {  
  switch (state) {  
    case START:  RB6 = 0;    //turn off LED  
                 while(RB3); //wait for press  
                 state = ON;  
                 break;  
    case ON:     RB6 = 1;    //turn on LED  
                 while(!RB3); //wait for release  
                 if (RB7) state = START; else state = BLKA;  
                 break;  
    case BLKA:  while(RB3) { //while not pressed  
                 if (LB6) LB6 = 0; else LB6 = 1; //blink  
                 DelayMs(200);  
                 }  
                 state = BLKB;  
                 break;  
    case BLKB:  while(!RB3) { //while pressed  
                 if (LB6) LB6 = 0; else LB6 = 1; //blink  
                 DelayMs(200);  
                 }  
                 state = START;  
                 break;  
  }  
}
```

- c. ( 20 pts) For the LED/Switch configuration shown in Figure (c) implement the actions listed in the figure in an *interrupt driven manner*. Divide your solution into two code segments -- an ISR, and *main()* code that includes the declarations of any variables used by the ISR initialization code for the interrupt system, initialization code for the ports, and code that initializes the LEDs to A=ON, B=OFF, C=OFF. You do not have to debounce the switch inputs in your ISR. You cannot have any delay code in the ISR that waits for input. Your infinite loop in *main()* has to be an infinite loop that is an EMPTY infinite loop *while(1){}*; your ISR has to do all of the work. **Your code must compile cleanly!!!!!!**

1. (14 pts) ISR code (do not worry about debouncing the switch inputs).

<pre> <b>interrupt my_isr() {</b>   <b>if (INT1IF &amp;&amp; INT1IE) {</b>     <b>INT1IF = 0;</b>     <b>if (LB7) {</b>       <b>LB7 = 0; LB6 = 1;</b>     <b>} else if (LB6) {</b>       <b>LB6 = 0; LB5 = 1;</b>     <b>} else if (LB5) {</b>       <b>LB5 = 0; LB7 = 1;</b>     <b>}</b>     <b>count++;</b>   <b>}//end if (INT1IF..)</b> </pre>		<pre> <b>//ISR continued</b> <b>if (INT12F &amp;&amp; INT12E) {</b>   <b>INT2IF = 0;</b>   <b>if (LB7) {</b>     <b>LB7 = 0; LB5 = 1;</b>   <b>} else if (LB6) {</b>     <b>LB6 = 0; LB7 = 1;</b>   <b>} else if (LB5) {</b>     <b>LB5 = 0; LB6 = 1;</b>   <b>}</b>   <b>count++;</b> <b>}//end if (INT2IF..)</b> <b>if (count == 10) {</b>   <b>//disable interrupts</b>   <b>INT1IE = 0; INT2IE = 0;</b> <b>}</b> <b>}//end ISR</b> </pre>
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2. (6 pts) *main()* code, your *while(1){}* has to be empty; the ISR must do all of the work.

<pre> <b>main() {</b>   <b>TRISB = 0x1F; //RB7, RB6, RB5 outputs, rest are inputs</b>   <b>IPEN = 0;</b>   <b>INTEDG1 = 1; INTEDG2 = 1; //rising edge trigger</b>   <b>INT1IF = 0; INT1IE = 1; //clear flag, enable interrupt</b>   <b>INT2IF = 0; INT2IE = 1;</b>   <b>GIE = 1; //enable interrupts</b>   <b>while(1); //empty infinite loop, ISR does work</b> <b>}</b> </pre>
--

- d. (7 pts) Assume an asynchronous serial channel with a data format of 1 start bit, 8 data bits, and 1 stop bit between characters. What is the minimum time in microseconds that it takes to send 20 characters at 57,600 baud?

$$\begin{aligned}
 10 \text{ bits (8, start, stop) } * 20 \text{ chars} * 1/57600 &= 3.472 \text{ e } -3 \\
 &= 3.472 \text{ ms} \\
 &= 3472 \text{ } \mu\text{s}
 \end{aligned}$$

- e. (7 pts) Write *C* code that implements the *char getch(void)* function that waits for a character to be available in the USART and then returns that character from the serial port. *No interrupts are enabled.*

```

char getch(void) {
    while (!RCIF); //wait for character
    return(RCREG); //return character
}

```

- f. (9 pts) Assume the definitions of a circular buffer that we have used in lab (i.e, the head pointer is used to place data into the buffer, the tail pointer is used to take data out of the buffer, the buffer is empty when head is equal to tail, and that pointers are incremented and wrapped before used to access the buffer).

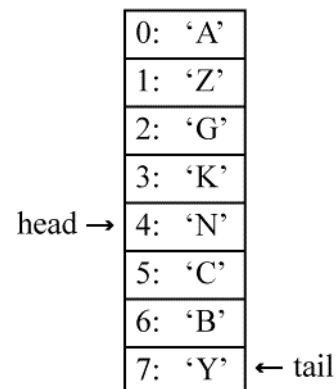
f1. From figure F, how many characters are currently **available** in the buffer? (this is not the total number of locations in the buffer) 5 chars (A, Z, G, K, N)

f2. From figure F, what character is returned if the buffer is read?

'A' (remember that tail is incremented, wrapped before reading!!)

f3. From figure F, what location is modified if one character is written to the buffer? location 5, since head incremented and wrapped before writing.

Problem (f)



- g. (12 pts) Given the code below and Figure (g), answer the questions below. The instruction code produces a square wave on RB5 as the *main*{ *while*(1) loop sets RB5 high and the ISR resets RB5 low.

For the following execution time components, check whether or not they are part of the LOW PULSE WIDTH TIME or HIGH PULSE WIDTH TIME of the square wave on RB5 (or check both if you think they belong to both):

	HIGH PW time	LOW PW time
a. execution time for instruction sequence A	_____	___XX___
b. execution time for instruction sequence B	_____	___XX___
c. execution time for instruction sequence C	___XX___	_____
d. execution time for instruction sequence D	_____	___XX___
e. interrupt service routine entry time	___XX___	_____
f. interrupt service routine exit time	_____	___XX___

```

interrupt my_isr() {
    if (INT1IF) {
        INT1IF = 0;

        //instruction
        //.....sequence
        //.....C (not shown)
        RB5 = 0;
        //instruction
        // .....sequence
        // .....D (not shown)
    }
} //end my_isr()

main() {

//....code that initializes INT1 to be RISING EDGE triggered
// ...and enabled, and RB5 to be initially low
while (1) {
    //...instruction
    //.....sequence
    //.....A (not shown)
    RB5 = 1;
    //...instruction
    //      sequence
    //      B      (not shown)
} // end while()
} //end main()

```

Part II: (24 pts) Answer 6 out of the next 8 questions. Cross out the 2 questions that you do not want graded. Each question is worth 4 pts.

1. Assume that your PIC micro current consumption is 20 mA at 40 MHz and 5 V. If the voltage is kept at 5V, what frequency should produce a new current consumption of 10 mA? If the frequency is kept at 40 MHz, what new voltage should produce a current consumption of 10 mA? Use only the formula for dynamic power consumption to compute these results; ignore static power consumption.

**Current is proportional to frequency, 20 MHz should reduce the current to 10 mA.**

$$I = V_{dd}^2 * Freq * C$$

$$C = 20 \text{ mA} / (25 * 40 \text{ MHz}).$$

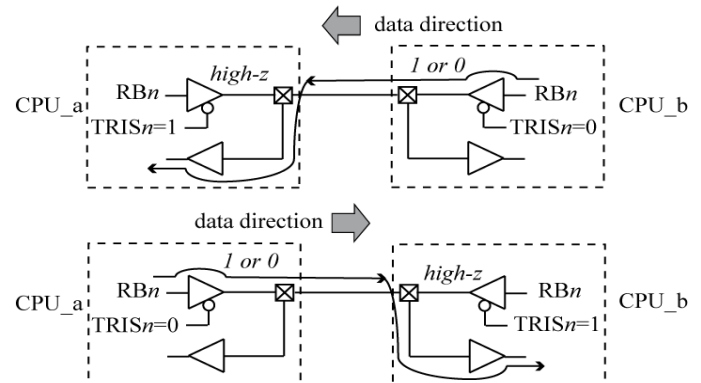
$$10 \text{ mA} = V_{dd}^2 * 40 \text{ MHz} * (20 \text{ mA}) / (25 * 40 \text{ MHz})$$

$$V_{dd}^2 = 10 \text{ mA} / 20 \text{ mA} * 25$$

$$V_{dd} = \text{square\_root}(25/2) = 3.5 \text{ V}$$

**So, reducing the voltage to 3.5V but keeping Freq =40 Mhz should reduce current to 10 mA**

2. Draw a diagram that shows how tri-state buffers (TSB) are used to implement a single-wire, bi-directional IO, half-duplex link. Show one diagram that has data flowing left to right, and a second diagram that has data flowing right to left. Show all of the terminals of the TSB and values for the terminals as appropriate.



3. In the code below, a student is trying to debounce a switch on INT1 that generates an interrupt by using a delay in the ISR as was done in lab. What is wrong? Correct the code. Explain the reasoning behind your answer.

```
interrupt my_isr () {
    if (INT1IF) {
        INT1IF = 0; //clear flag
        DelayMsISR(20) // delay 20 ms to debounce
        //rest of code not shown
    }
}
```

**The “DelayMsISR” should be BEFORE the “INT1IF=0;” statement to give the switch time to settle before clearing the flag. As it is right now, the debounce delay is not serving any purpose.**

4. What is the SPBRG value for a baud rate of 19200 assuming an FOSC of 20 MHz and low speed mode?

$$[20 \text{ MHz} / (19200 * 64)] - 1 = 15.3, \text{ round to } 15.$$

5. In the code below, give what is printed to the console assuming the standard PIC18 setup that you have been using in lab. Explain the reasoning behind your answer.

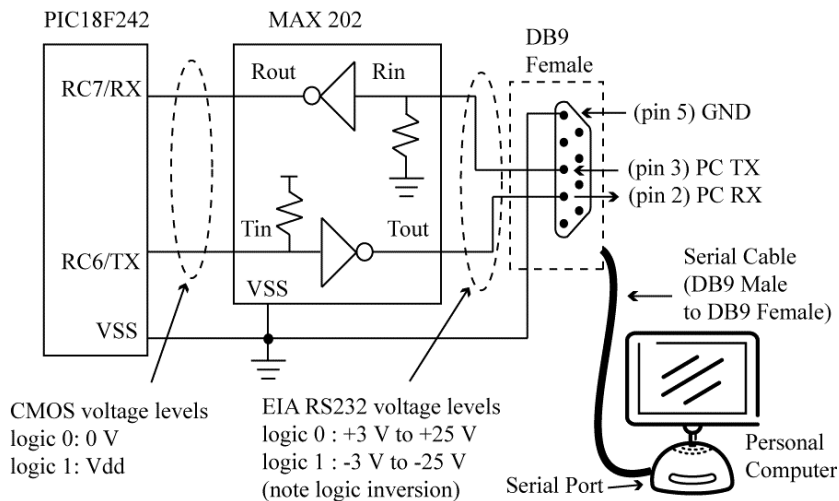
```
main() {
    serial_init(95,1); // 19200 in HSPLL mode, crystal = 7.3728 MHz
    SWDTEN = 1;
    while (1) {
        printf("Yawn");pcrf();
        DelayMs(30); //give time for chars to finish printing
        asm("sleep");
        SWDTEN = 0;
        printf("Huh?");pcrlf();
    } //end while()
} //end main()
```

This will print:

"Yawn"  
"Huh?"  
"Yawn".

After the first 'Yawn', the micro sleeps, but the watchdog timer wakes it since the watchdog timer is enabled before the loop is entered. The WDT is then disabled by SWDTEN=0, so after going back to the top of the loop and after the second sleep, the processor does not wake back up.

6. Draw a diagram that illustrates the basics of how your PIC micro is connected to the serial port of the PC.



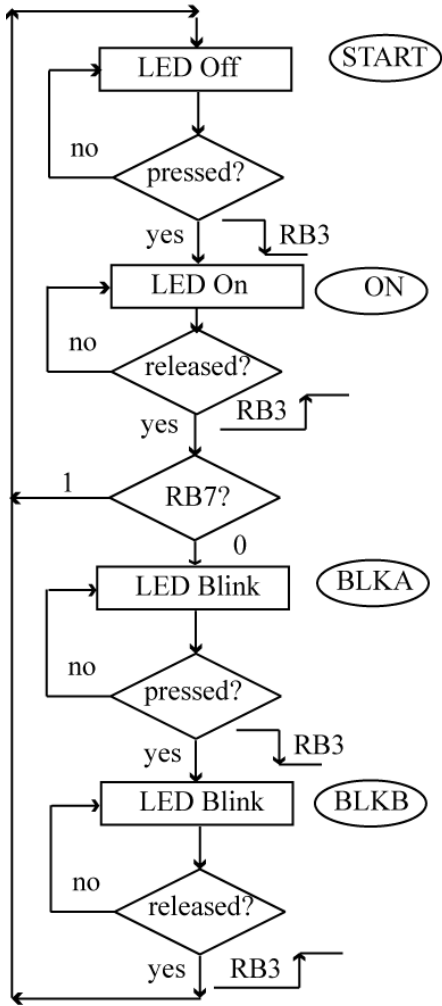
7. Write a C code fragment that detects a power up of the PIC18 and prints the message “Power On”. Be sure that you do not detect falsely detect a power up.

```
if (!POR) {  
    POR = 1;  
    printf(“Power on\n\r”);  
}
```

8. In an asynchronous serial interface, what would prevent me from having a data format of 16 data bits + start + stop? Or a format of 32 data bits + start + stop? Or a format of 1000 data bits + start + stop? , i.e., why can't I send an arbitrary number of data bits during an asynchronous data transmission?

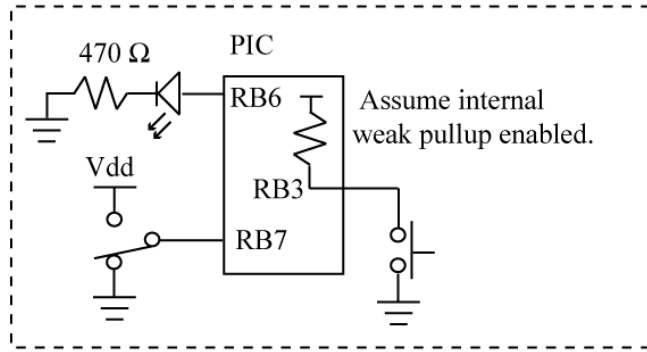
**No clock signal is sent with the data. Even though both CPUs are generating clocks of the same frequency in order to input the data, the clocks will have some small percent mismatch (it is not possible to match the clocks perfectly). Over several bit times, this causes a cumulative error to build up in how a CPU is determining the midpoint of a bit time, and eventually the receiving CPU will be sampling the input waveform in the wrong place.**

Problem (b)



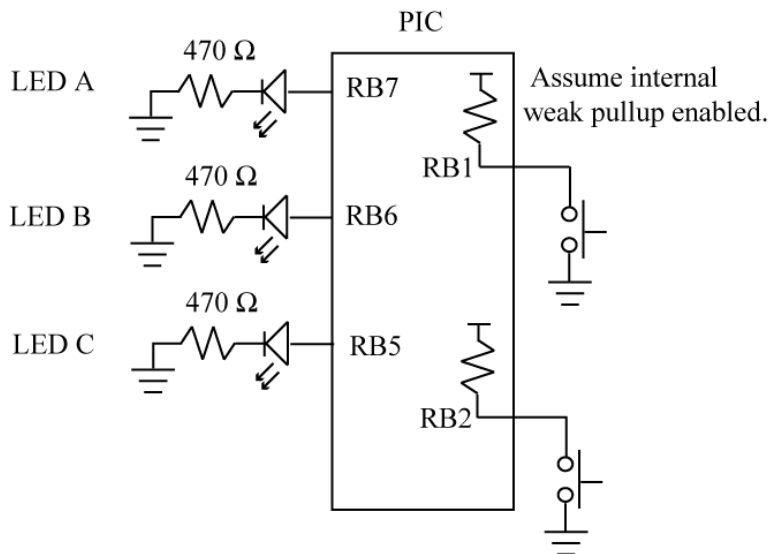
Figures

LED Switch IO Problem (a)

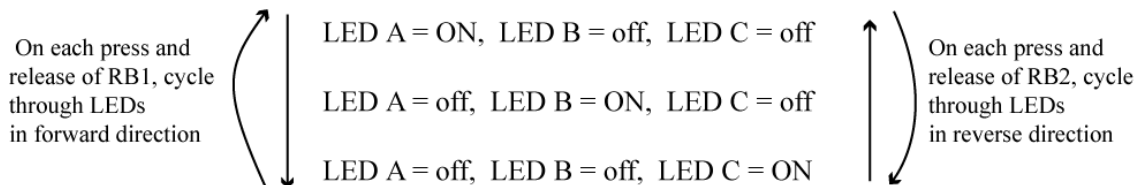


Pushbutton  
input for RB3

Problem (c)



Actions for Problem (c). Assume LEDs are initially A = ON, B = off, C = off (initialized by main)



LED changes only happen on a press and release of either RB1 or RB2.

After a total of TEN button press/releases, disable both the RB1 and RB2 interrupts. Button presses of either RB1 or RB2 can happen in any order.

Do not worry about simultaneous button presses.

(Implementation hint: read the value of an LED output to determine what to do next)

An example sequence:

1. LEDs initially A=ON, B=off, C=off.
2. press and release of RB2
3. A=off, B=off, C=ON
4. press & release of RB2
5. A=off, B=ON, C=off
6. press & release of RB1
7. A=off, B=off, C= ON
8. press & release of RB1
9. A=ON, B=off, C=off
- etc....

An example sequence:

1. LEDs initially A=ON, B=off, C=off.
2. press and release of RB1
3. A=off, B=ON, C=off
4. press & release of RB1
5. A=off, B=off, C=ON
6. press & release of RB1
7. A=ON, B=off, C= off
8. press & release of RB2
9. A=off, B=off, C=ON
10. press & release of RB2
11. A=off, B=ON, C=off

